Electronic Acknowledgement Receipt					
EFS ID:	4068578				
Application Number:	10715267				
International Application Number:					
Confirmation Number:	4590				
Title of Invention:	Methods for polishing semiconductor device structures by differentially applying pressure to substrates that carry the semiconductor device structures				
First Named Inventor/Applicant Name:	Nathan R. Brown				
Customer Number:	24247				
Filer:	Brick Glenn Power/Nancy Johnson				
Filer Authorized By:	Brick Glenn Power				
Attorney Docket Number:	2269-4375.1US (99-1029.01				
Receipt Date:	06-OCT-2008				
Filing Date:	17-NOV-2003				
Time Stamp:	20:36:19				
Application Type:	Utility under 35 USC 111(a)				

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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)			
1	Request for Continued Examination (RCE)	10-6-08-RCE.pdf	286338 6b1651b8026941b4d349c0913510f75aa80	no	3			
			1a204					
Warnings:								
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2	2	10-6-08-AmendAccRCE.pdf	840042	yes	13			
2		10 0 00 fulleriar (certezipar	eb1e0ce849f37fce30b709ab8a2f99cc77fb0 2ff					
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	Document Description		Start	End				
	Amendment Submitted/Entered with Filing of CPA/RCE		1	1				
	Claims		2	6				
	Applicant Arguments/Remarks Made in an Amendment		7	13				
Warnings:								
Information:								
3 Fee Worksheet (PTO-06)	fee-info.pdf	30656	no	2				
		c8f237c506cbe7047ae778e43cf23505f45fc 4e5						
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